

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S45	34	"327"/\$.ccls. and (voltage same cpu).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 12:37
S98	514	"327"/\$.ccls. and voltage same cpu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 12:38
S10 0	5	("6035407"   "6601179"   "6691235"   "6697952"   "6732266").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 12:48
S10 3	9	"714"/\$.ccls. and (voltage and processor).ab. and @pd>"20041001"	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 12:51
S10 1	3	"714"/\$.ccls. and (voltage and cpu).ab. and @pd>"20041001"	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 12:51
S10 4	28	"324"/\$.ccls. and (voltage and processor).ab. and @pd>"20041001"	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 12:52
S10 2	8	"324"/\$.ccls. and (voltage and cpu).ab. and @pd>"20041001"	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 12:52
S10 5	7	("4992970"   "5184059"   "5283762"   "5355073"   "5384692"   "5421734"   "5422523").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 13:01
S10 6	6	("5632039").URPN.	USPAT	OR	ON	2006/02/17 13:06
S99	1	"327"/\$.ccls. and voltage same cpu and south near bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 13:20
S10 7	111	voltage same (cpu processor) and south\$1bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 13:57
S10 9	1838	south\$1bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 13:58

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S108	0	south\$1bridge same voltage same error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 13:58
S111	70	south\$1bridge same voltage and (error fault fail\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:04
L4	4	((("6836848") or ("6946731") or ("20050263849") or ("20030097515")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	OFF	2006/02/17 15:36
S110	81	south\$1bridge same voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:42
L6	779	detect\$3 near3 voltage same (cpu processor) and bridge	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:43
L5	8798	detect\$3 near3 voltage same (cpu processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:43
L8	2	detect\$3 near3 voltage same (cpu processor) and floating near3 error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:44
L7	2478	detect\$3 near3 voltage same (cpu processor) and error	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:44
L10	13	"ferr#" same voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:46
L9	5	floating adj point near3 error same voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/17 15:46

## EAST Search History

L11	24	("3721961"   "4075693"   "4281392"   "4319343"   "4443846"   "4484273"   "4674089"   "4716526"   "4860252"   "4862355"   "4899306"   "4908789"   "4951248"   "4964074"   "4975838"   "5093908"   "5101342"   "5121486"   "5138708"   "5202976"   "5287460"   "5297272"   "5325490"   "Re31318").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/17 15:56
L13	1	(floating adj point adj error same voltage).clm.	US-PGPUB; USPAT	OR	ON	2006/02/17 16:09
L12	19	("5551012").URPN.	USPAT	OR	ON	2006/02/17 16:09



southbridge detect cpu voltage

Search

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M Fleischmann - White Paper of Transmeta Corporation, January, 2001 - ececs.uc.edu

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... CMI8738. Podporuje ASUS iPanel. Viaka technológií ASUS COP (**CPU** Overheating Protection) chráni procesory pred prehriatím. FSB ...[View as HTML](#) - [Web Search](#)[Transmeta Crusoe](#)

PP Series, L Harrison - cs.uiuc.edu

... 14 Processor Block Diagram **CPU** core Integer Unit ... **PCI** Controller & **Southbridge** Interface Page 15. ... Except: Complete two-cycle ALU0/ALU1 ops and **detect** exceptions ...[View as HTML](#) - [Web Search](#)[3U Embedded CompactPCI® 586-based PC](#)

U Manual - steatite.co.uk

... **Southbridge** ... An on-board regulator is used to generate the 3.3V **voltage** for the processor and the other 3.3V devices (switched mode ... 2.3 STPC Industrial **CPU** Core ...[View as HTML](#) - [Web Search](#)[ZOOPLANKTON VISUALIZATION SYSTEM: DESIGN AND REAL-TIME LOSSLESS IMAGE COMPRESSION](#)

D Reddy - 2004 - etd.lsu.edu

... CLB Control Logic Block CMOS Complementary Metal Oxide Semiconductor **CPU** Central Processing ... Pixels LUT Look up tables LVDS Low **Voltage** Differential Signaling M ...[View as HTML](#) - [Web Search](#)[TRIDENT MICROSYSTEMS, INC. - group of 2 »](#)

X Yes - tridentmicro.com

... 1-licensed Northbridge and Ali's Mobile **Southbridge**. ... The chip is designed with external **CPU(s)** and ... progressive scan, and adaptive motion **detect** capability by ...[View as HTML](#) - [Web Search](#)[\[book\] Hacking the Xbox: An Introduction to Reverse Engineering - group of 2 »](#)

A "bunnie" Huang - 2003 - books.google.com

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SJ Kang, M Sly - it.korea.ac.kr

... Affects the Backend throughput Power Requirements Maximum, Typical, Standby **Voltage** Levels 5V ... tasks to run, the EPOC32 operating system halts the **CPU** clock by ...[View as HTML](#) - [Web Search](#)[AMD-768™ Peripheral Bus Controller Data Sheet - group of 9 »](#)